

EXPEDITED PROCEDURE – EXAMINING GROUP 2826

S/N 09/893023

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Frank O'Mahony et al. Examiner: Ahmed Sefer
Serial No.: 09/893023 Group Art Unit: 2826
Filed: June 27, 2001 Docket No.: 884.405US1
Title: LOW LOSS INTERCONNECT STRUCTURE FOR USE IN
MICROELECTRONIC CIRCUITS
Assignee: Intel Corporation Customer No.: 21186

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the final Office Action mailed July 16, 2003 and the Advisory Action mailed on October 9, 2003, please amend the application as follows: